

### **Specification Amendments**

Please replace the paragraph beginning on page 24, line 26, with the amended paragraph, as follows:

FIG. 32 is a schematic block diagram of power supply rejection circuits for triangle and sine wave embodiments, respectively.

Please replace the paragraph beginning on page 39, line 25, with the amended paragraph, as follows:

The power supply noise/ripple rejection approach of Figure 32 is used in the Analog ModAmp. It was shown in the invention disclosure document that if the reference oscillators' amplitudes are controlled in proportion to the MOSFET power supply voltage, then we achieve an output that is independent of the supply voltage.